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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/893,964	06/29/2001	Yun Bok Lee	8733.452.00	6107

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EXAMINER

RAO, SHRINTVAS H

ART UNIT

PAPER NUMBER

2814

DATE MAILED: 09/29/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/893,964	LEE ET AL.
	Examiner Steven H. Rao	Art Unit 2814
<i>-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --</i>		
Period for Reply		
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.		
<ul style="list-style-type: none"> - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). 		
Status		
1) <input checked="" type="checkbox"/> Responsive to communication(s) filed on <u>22 July 2003</u> .		
2a) <input checked="" type="checkbox"/> This action is FINAL. 2b) <input type="checkbox"/> This action is non-final.		
3) <input type="checkbox"/> Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.		
Disposition of Claims		
4) <input checked="" type="checkbox"/> Claim(s) <u>1-30</u> is/are pending in the application.		
4a) Of the above claim(s) _____ is/are withdrawn from consideration.		
5) <input type="checkbox"/> Claim(s) _____ is/are allowed.		
6) <input type="checkbox"/> Claim(s) <u>1-30</u> is/are rejected.		
7) <input type="checkbox"/> Claim(s) _____ is/are objected to.		
8) <input type="checkbox"/> Claim(s) _____ are subject to restriction and/or election requirement.		
Application Papers		
9) <input type="checkbox"/> The specification is objected to by the Examiner.		
10) <input type="checkbox"/> The drawing(s) filed on _____ is/are: a) <input type="checkbox"/> accepted or b) <input type="checkbox"/> objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).		
11) <input type="checkbox"/> The proposed drawing correction filed on _____ is: a) <input type="checkbox"/> approved b) <input type="checkbox"/> disapproved by the Examiner. If approved, corrected drawings are required in reply to this Office action.		
12) <input type="checkbox"/> The oath or declaration is objected to by the Examiner.		
Priority under 35 U.S.C. §§ 119 and 120		
13) <input type="checkbox"/> Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) <input type="checkbox"/> All b) <input type="checkbox"/> Some * c) <input type="checkbox"/> None of: 1. <input type="checkbox"/> Certified copies of the priority documents have been received. 2. <input type="checkbox"/> Certified copies of the priority documents have been received in Application No. _____. 3. <input type="checkbox"/> Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.		
14) <input type="checkbox"/> Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application). a) <input type="checkbox"/> The translation of the foreign language provisional application has been received.		
15) <input type="checkbox"/> Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.		
Attachment(s)		
1) <input type="checkbox"/> Notice of References Cited (PTO-892)		
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)		
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.		
4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s) _____.		
5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)		
6) <input type="checkbox"/> Other: _____.		

Response to Amendment

Applicants' amendment filed on July 17, 2003 has been entered on July 22, 2003.

Therefore claims 1, 10 and 19 as amended by the amendment and claims 2-9,11-18 and 20-30 as recited in the amendment are currently pending in the Application.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1,3 and 8 are rejected under 35 U.S.C. 102(b) as being anticipated by Lee et al. (U.S. Patent No. 5,946,066, hereinafter Lee).

With respect to claim1 Lee describes a liquid crystal display device comprising: a plurality of gate lines (Lee fig. 3 A # 11, col.2 line 41-42) a plurality of data lines, crossing said gate lines, such that at least one pixel region is defined by the data and gate lines, (fig. 3 A # 12, col. 2 lines 42-45) at least one of said data lines defining the pixel region having a first data line section and a second data line section, the first data line section and the second data line section intersecting at a data line bent portion (fig. 3A # 12 is bent) at least one data electrode in the pixel region, the data electrode having a first data electrode section and a second data electrode section, the first data electrode section and the second data electrode section intersecting at a

data electrode bent portion; (Lee figure 3a # 13, col. 2 lines 60-65) at least one common electrode in the pixel region, the common electrode having a first common electrode section and a second common electrode section, the first common electrode section and the second common electrode section intersecting at a common electrode bent portion; (Lee fig. 3 A #14, col. 2 lines 58-61) and at least one common line in the pixel region (Lee col.2 lines 63-64), the common electrode crossing the data lines, the data electrode, and the common electrode. (Lee figs. 3a, b etc.) and wherein the common line is formed on a different layer from the gate electrode (Lee figure 3A, col.2 lines 60-65)..

With respect to claim 3 Lee describes the liquid crystal display device of claim 1, wherein the common line is substantially parallel to the gate line. (Lee figs. 3 a,b).

With respect to claim 8 Lee describes the liquid crystal display device of claim 1, wherein the data electrodes partially overlap at least one of the gate lines. (Lee figs. 3 a, b).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Art Unit: 2814

Patentability shall not be negated by the manner in which the invention was made.

Claims 2, 4-7 and 9 to 30 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lee et al. (U.S. Patent No. 5,946,066, hereinafter Lee) as applied to claim 1 above and further in view of Asada et al. (U.S. Patent No. 5,745,207, herein after Asada).

With respect to claim 2, Lee describes the liquid crystal display device of claim 1. wherein the common line crosses the data line at the data line bent portion. (Lee 3b # 11 intersecting 12, col. 2 lines 42-44).

Lee does not specifically state the common line crosses data electrode at the data electrode bent portion, and the common electrode at the common electrode bent portion.

However, Asada in Figure 3 and col. 6 lines 56-65 describes the common line crosses data electrode at the data electrode bent portion, and the common electrode at the common electrode bent portion to compensate the coloring corresponding to the angle of view and the total non-display area can be reduced.

Therefore it would have been obvious to one of ordinary skill in the art at the time of the invention to include Asada's arrangement of the common line crosses data electrode at the data electrode bent portion, and the common electrode at the common electrode bent portion In Lee's device to compensate the coloring corresponding to the angle of view and the total non-display area can be reduced. (Asada col. 3 lines 60- to col. 4 lines 5).

Art Unit: 2814

With respect to claim 4 Lee describes the liquid crystal display device of claim 1, wherein the first data line section, the first data electrode section, and the first common electrode section are substantially parallel. (Lee figures 3 a,b).

With respect to claims 5 to 7 Lee describes the liquid crystal display device of claim 1, wherein the common lines , data electrodes and common electrodes comprise a transparent conductive material (Asada col. 1 line 34-35).

With respect to claim 9 Lee describes the liquid crystal display device of claim 1, wherein a storage capacitor is formed where the common line crosses the data electrode. (Asada col. 1 line 40).

With respect to claim 10 Lee describes a liquid crystal display device comprising: a plurality of gate lines; a plurality of data lines, crossing said gate lines, such that at least one pixel region is defined by the data and gate lines, at least one of said data lines defining the pixel region, the data lines having a plurality of data line segments, the data line segments intersecting at data line bent portions; at least one data electrode in the pixel region, the data electrode having a plurality of data electrode segments, the data electrode segments intersecting at common electrode bent portions; and at least one common electrode in the pixel region, the common electrode having a plurality of common electrode segments, the segments intersecting at common electrode bent portions; and at least one light shielding layer on the pixel region, the light shielding layer crossing the data lines the data electrode, and the common electrode at respective ones of the data line bent portions, the data electrode bent portions and the common electrode bent portions wherein the light shielding layer is

Art Unit: 2814

formed on a different layer from gate lines. (Lee figure 3 B , col.3 lines 8-15) and are rejected for the same reasons as claims 1-9 above) .

With respect to claim 11, Lee The device of claim 10, wherein one of the light shielding layers is a common line. (Lee col. 2 lines 63-65 and Asada col.5 line30)

With respect to claim 12 Lee describes An in-plane switching mode liquid crystal display device comprising: gate lines formed on a substrate; data lines having a plurality of bent portions to cross the gate lines, the data and gate lines defining a pixel region; a plurality of data electrodes and common electrodes having a plurality of bent portions; a common line on the bent portions of the data lines, the data electrodes and the common electrodes; and a plurality of auxiliary common lines on the bent portions of the data electrodes and the common electrodes. Rejected for same reasons as claims 1 and 10 above).

With respect to claim 13 Lee describes the device of claim 12, wherein the common lines are formed in parallel with the gate lines. (Asada fig. 2,etc.).

With respect to claims 14 to 16 Lee describes the device of claim 12, wherein the common lines, data electrodes and common electrodes include transparent conductive film. (rejected for the same reasons as set out under claims 5-7 above).

With respect to claim 17 Lee describes the device of claim 12, wherein at least one of the common lines is formed within the pixel region. (rejected for the same reasons as stated under claim1 above).

Art Unit: 2814

With respect to claim 18 Lee describes the device of claim 12, wherein the data lines, the data electrodes, and the common electrodes have at least one bent portion. (Lee figs. 3a,b)

With respect to claim 19 Lee describes the device of claim 12, wherein the common lines are formed integrally with the common electrodes. (Asada fig. 2 etc.)

With respect to claim 20 Lee describes the device of claim 12, wherein the data electrodes are connected with the gate lines. (Lee col.2 lines 62-65, Asada fig. 2)

With respect to claim 21 Lee describes the device of claim 12, wherein the data electrodes overlap the gate lines.(Lee figs. 3a,b and Asada fig.2).

With respect to claim 22 Lee describes the device of claim 12, wherein the data and common electrodes are on different layers. .(Lee figs. 3a,b and Asada col.1 lines 41-65).

With respect to claim 23, Lee describes the device of claim 12, further comprising a light shielding layer on the bent portion between the data electrodes and the common electrodes. (Lee col. 2 lines 63-65 and Asada col.5 line30).

With respect to claim 24 Lee describes an in-plane switching mode liquid crystal display device, comprising: gate lines on a substrate; data lines having a plurality of bent portions to cross the gate lines, the data and gate lines defining a pixel region; a plurality of data electrodes having a plurality of bent portions; common electrodes having a plurality of bent portions, the common electrodes being connected with each other at the bent portions; and common lines on the bent portions of the data lines, the

Art Unit: 2814

data electrodes and the common electrodes. (rejected for the same reasons as claims 1,10 and 12 above).

With respect to claim 25 Lee describes the device of claim 24, wherein the data electrodes are connected with the gate lines. (Asada figure 2)

With respect to claim 26 Lee describes the device of claim 24, wherein the common electrodes are connected with the common lines. (Asada figs. 2, 3)

With respect to claim 27 Lee describes the device of claim 24, wherein the data electrodes include a first electrode and a second electrode. (Lee fig.3a,b etc. and Asada figs. 2)

With respect to claim 28 Lee describes the device of claim 27 wherein the first electrode has a plurality of bent portions. (Lee fig.3a,b etc. and Asada figs. 2)

With respect to claim 29 Lee describes the device of claim 27, wherein the first electrode is connected with the second electrode. (Lee fig.3a,b etc. and Asada figs. 2)

With respect to claim 30 Lee describes the device of claim 24, further comprising a light shielding layer on the bent portion between the data electrodes and the common electrodes. (Lee col. 2 lines 63-65 and Asada col.5 line 30).

Response to Arguments

Applicant's arguments filed on 7/22/2003 have been fully considered but they are not persuasive because Lee describes at least one common line in the pixel region, the common line crossing the data lines, the data electrode and the common electrode wherein the common line is formed on a different layer from the gate line as shown above in the rejection.

Art Unit: 2814

Similarly for reasons stated above in the rejections the combination of Lee and Asada teach/describe the following limitations :

at least one common line in the pixel region (Lee col.2 lines 63-64), the common electrode crossing the data lines, the data electrode, and the common electrode. (Lee figs. 3a, b etc.) and wherein the common line is formed on a different layer from the gate electrode (Lee figure 3A, col.2 lines 60-65)..

at least one light shielding layer on the pixel region, the light shielding layer crossing the data lines the data electrode, and the common electrode at respective ones of the data line bent portions, the data electrode bent portions and the common electrode bent portions wherein the light shielding layer is formed on a different layer from gate lines. (Lee figure 3 B , col.3 lines 8-15) and are rejected for the same reasons as claims 1-9 above) .

a plurality of auxiliary common lines on the bent portions of the data electrodes and the common electrodes. Rejected for same reasons as claims 1 and 10 above).

common lines on the bent portions of the data lines, the data electrodes and the common electrodes. (rejected for the same reasons as claims 1,10 and 12 above).

Therefore all the presently recited claim limitations are taught by the applied prior art of record.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

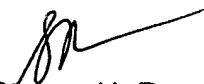
Art Unit: 2814

§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

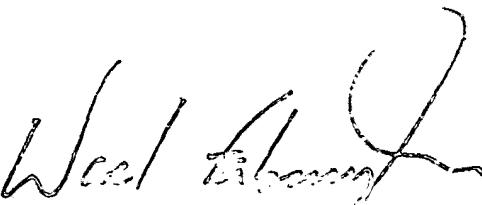
Any inquiry concerning this communication or earlier communication from the examiner should be directed to Steven H. Rao whose telephone number is (703) 306-5584. The examiner can normally be reached on Monday- Friday from approximately 7:00 a.m. to 5:30 p.m.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 308-0956. The Group facsimile number is (703) 308-7724.


Steven H. Rao

Patent Examiner

September 24, 2003.


Wael Alhammadi
SUT
TELECOMMUNICATIONS
TECHNOLOGY CENTER